PATENT
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N THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Hey, et al.

Serial No.: 09/853.962

Confirmation No.: 6593

Filed:

May 10, 2001

For:

Method of Application of

Electrical Biasing To

Enhance Metal Deposition

C ro

Group Art Unit: 1753

Examiner:

Robert R. Koehler

MAIL STOP Commissioner for Patents P.O. Box 1450

Alexandria, VA 22313-1450

CERTIFICATE OF MAILING 37 CFR 1.8

I hereby certify that this correspondence is being deposited on April 29, 2004 with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450.

4/29/2004

Signature

Dear Sir:

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The Applicants, and the Attorney who signs below on the basis of the information supplied by the inventor and the information in his file, submit herewith patents, publications, or other information of which they are aware, which may be material to the examination of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR § 1.56.

While the information submitted in this Supplemental Information Disclosure Statement may be material pursuant to 37 CFR § 1.56, it is not intended to constitute an admission that any patent, publication, or other information referred to therein is prior art for this invention unless specifically designated as such.

In accordance with 37 CFR § 1.97, this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made or that no other possibly material information as defined under 37 CFR § 1.56(a) exists.

The patents and/or publications submitted herewith are set forth on the attached Form PTO-1449.

If the sum of \$180.00 is due under 37 CFR § 1.17(p) pursuant to § 1.97, the Commissioner is hereby authorized to charge this fee, and any other fee necessary to make this submission timely, to the Deposit Account No. AMAT/4582/KMT.

Respectfully submitted,

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(Use as many sheets as necessary)

Sheet 1 of 2

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Application Number	09/853,962
iling Date	May 10, 2001
First Named Inventor	Hey, et al.
Group Art Unit	1753
Examiner Name	Robert R. Koehler
Attorney Docket Number	AMAT/4582/CMP/ECP/RKK
Submission Date	April 29, 2004

			U.S. PATENT	DOCUMENTS		
Examiner Initials*	Cite No.1	Document Number	Publication Date	Name of Patentee or	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
	INO.	Number-Kind Code ^{2 (if known)}	MM-DD-YYYY	Applicant of Cited Document		
	A1	US-3,627,661	02/13/1969	Gordon, et al.		
	A2	US-4,065,374	12/27/1977	Asami, et al.		
	А3	US-4,120,759	10/17/1978	Asami, et al.		
	A4	US-4,129,480	12/12/1978	Robert		
	A5	US-5,670,034	09/23/1997	Lowery		
	A6	US-6,133,061	10/17/2000	Sonoda		
	A7	US-6,162,344	12/19/2000	Reid, et al.		
	A8	US-6,349,633	02/26/2002	Inakuma, et al.		

		FORE	IGN PATENT	DOCUMENTS		
Examiner	Cite	Foreign Patent Document	Publication Date	Name of Patentee or	Pages, Columns, Lines,	T ⁶
Initials*	No.'	Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	MM-DD-YYYY	Applicant of Cited Document	Where Relevant Passages or Relevant Figures Appear	
	B1	52056369	05/09/1977	JP English Abstract		
	B2	1160846	12/05/2001	EP		

Examiner Date Considered

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT				ANT	Group Art Unit	1753	1
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		(Use as many sheets as ned	essary)	Attorney Docket Number	AMAT/4582/CMP/ECP/RKK	Ť
	Sheet	2	of	2	Submission Date	April 29, 2004	Ť

		NON PATENT LITERATURE DOCUMENTS			
Examiner Cite No.1		Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			
		Singer, P., "Tantalum, Copper and Damascene: The Future of Interconnects," Semiconductor International, June 1998, 6 pages.			
Examiner		Date Considered			

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